IMC6 Pin Muxing

Explain the pin muxing and pad control:

&iomuxc {

pinctrl-names = "default";

pinctrl-0 = <&pinctrl\_hog>;

hog {

pinctrl\_hog: hoggrp {

fsl,pins = <

/\* gpio-leds \*/

MX6QDL\_PAD\_EIM\_D28\_\_GPIO3\_IO28 0x80000000

MX6QDL\_PAD\_EIM\_A25\_\_GPIO5\_IO02 0x80000000

/\* J11 pins 32, 34, 36, 38 \*/

MX6QDL\_PAD\_CSI0\_DAT8\_\_GPIO5\_IO26 8

MX6QDL\_PAD\_CSI0\_DAT9\_\_GPIO5\_IO27 8

MX6QDL\_PAD\_KEY\_COL1\_\_GPIO4\_IO08 8

MX6QDL\_PAD\_KEY\_ROW1\_\_GPIO4\_IO09 8

>;

};

};

};

<https://github.com/FrankBau/meta-marsboard-bsp/wiki/iMX6-Pad-Mux-and-Pad-Control>

<https://github.com/torvalds/linux/blob/master/arch/arm/boot/dts/imx6dl-pinfunc.h>

<https://community.nxp.com/thread/323719>

<http://bec-systems.com/site/1313/understanding-the-nxp-i-mx6ul-pin-mux>